

PRIOR ART

FIG 1

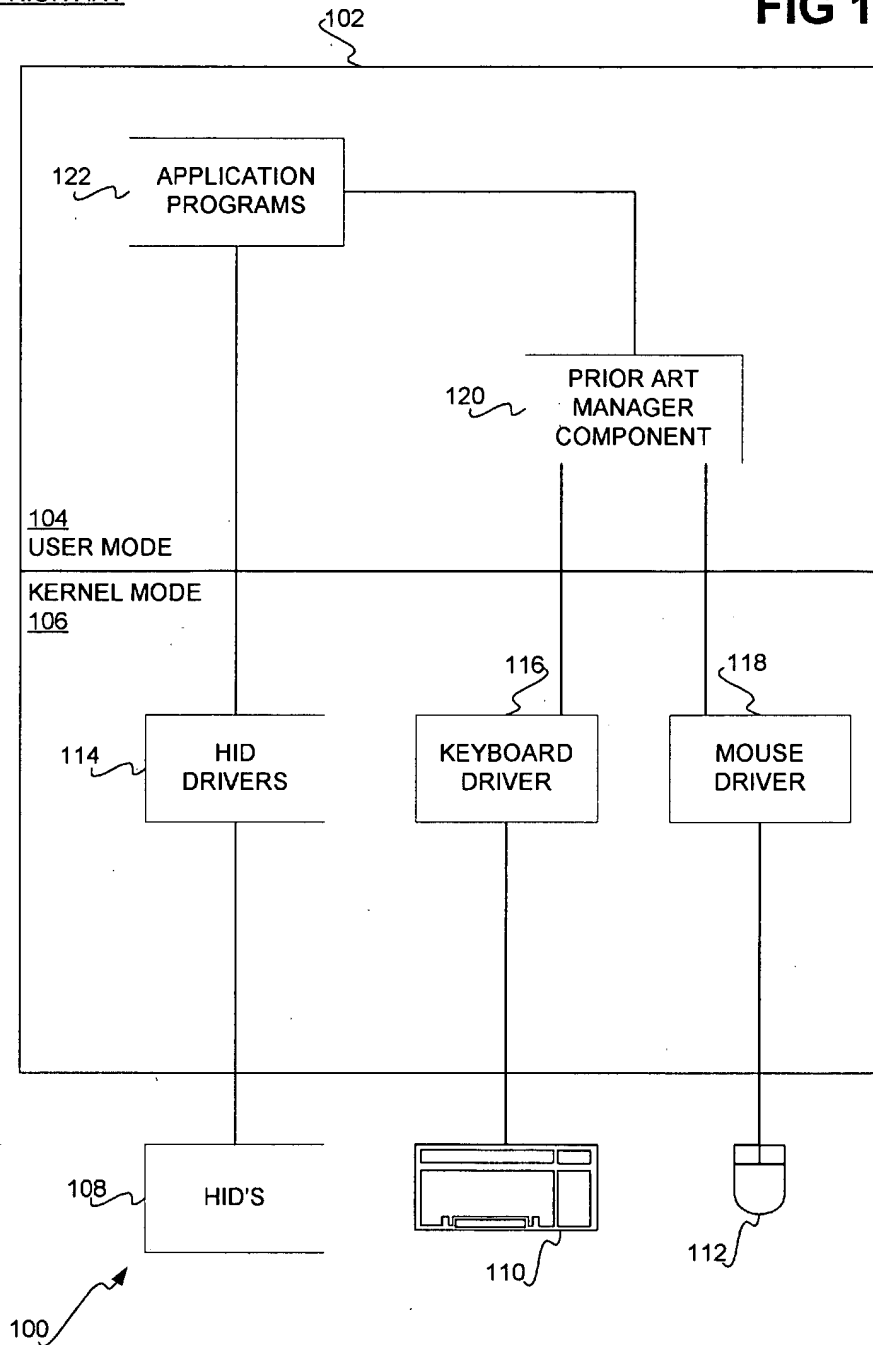
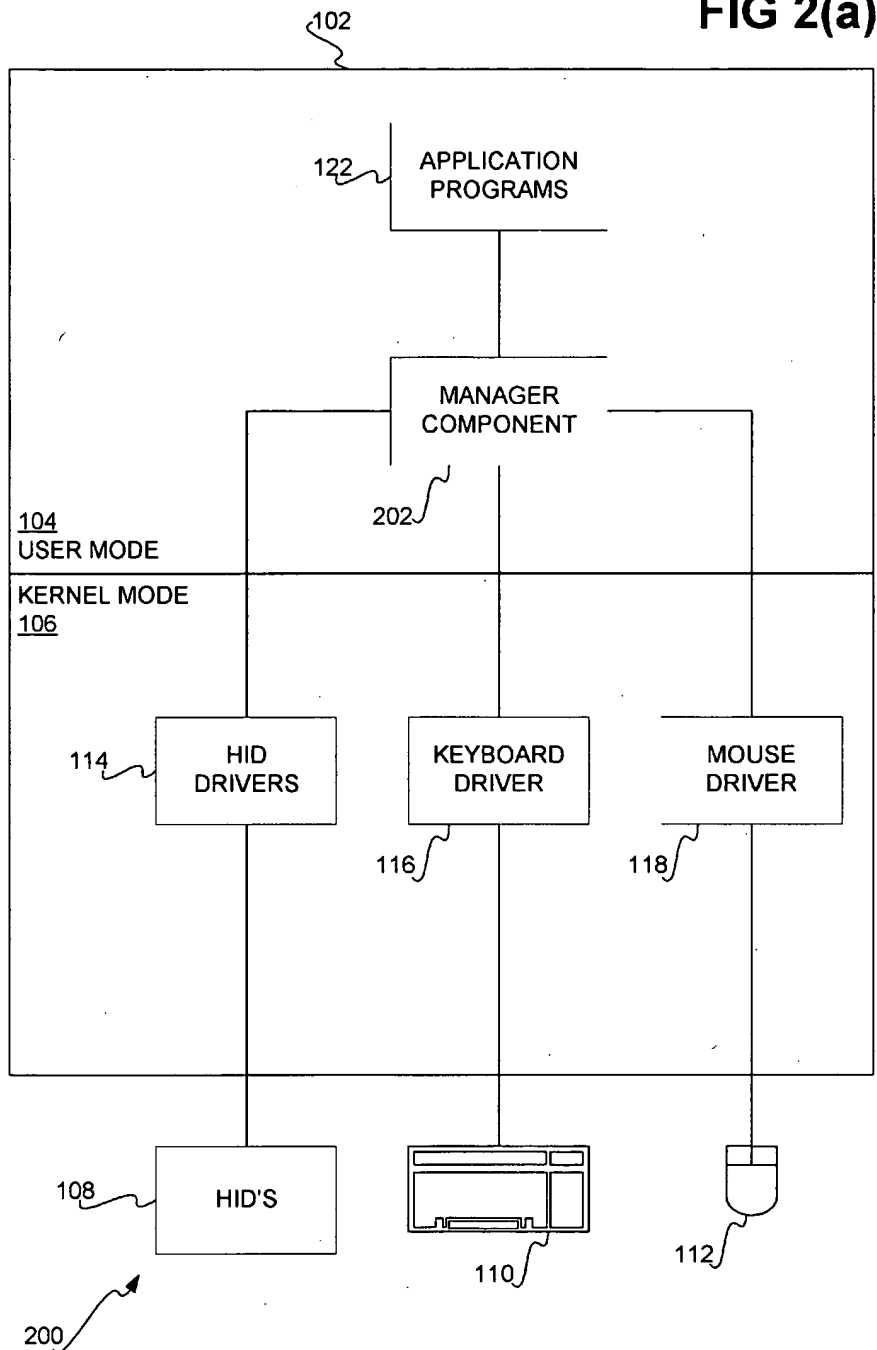


FIG 2(a)



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REGISTRATION

FIG 2(b)

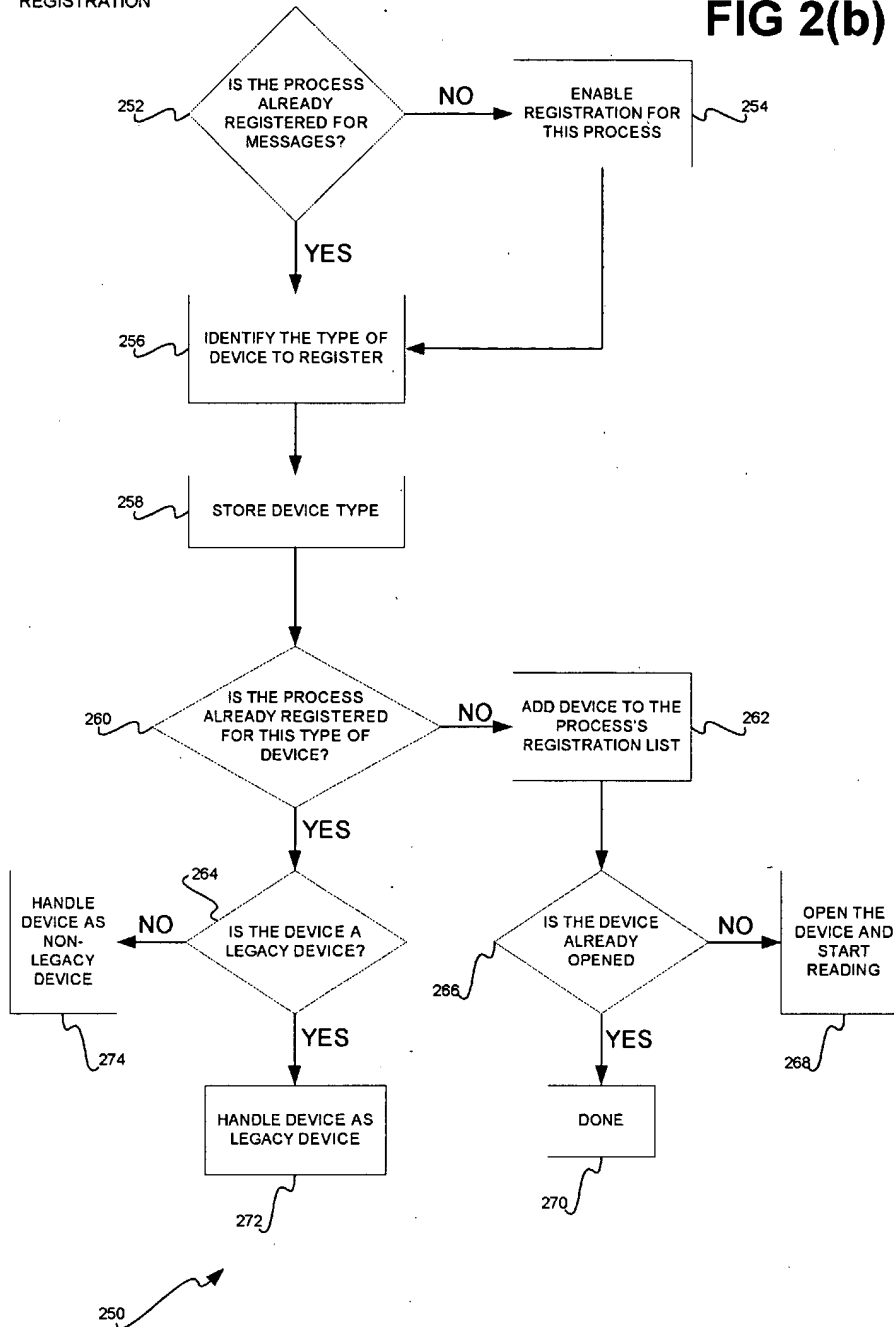
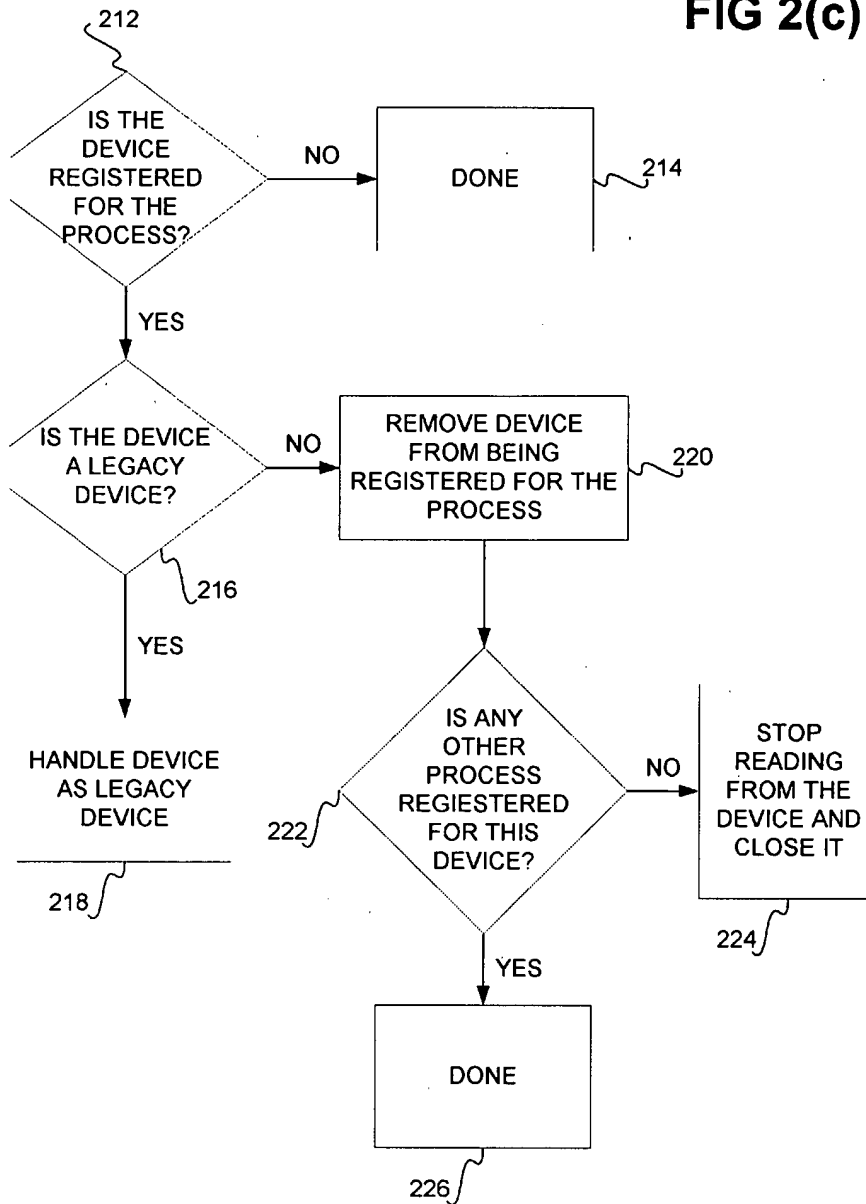
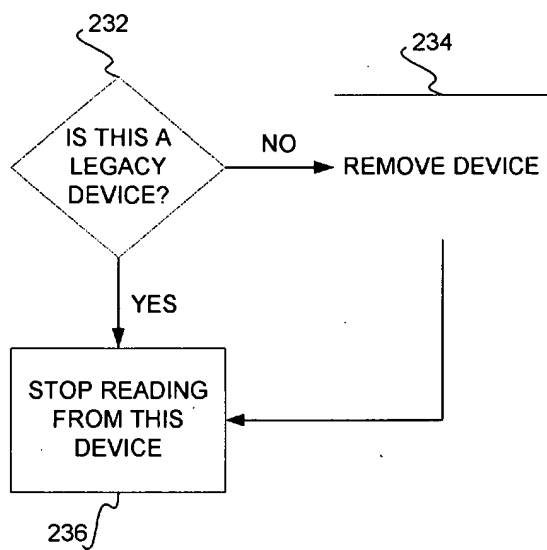


FIG 2(c)



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FIG 2(d)

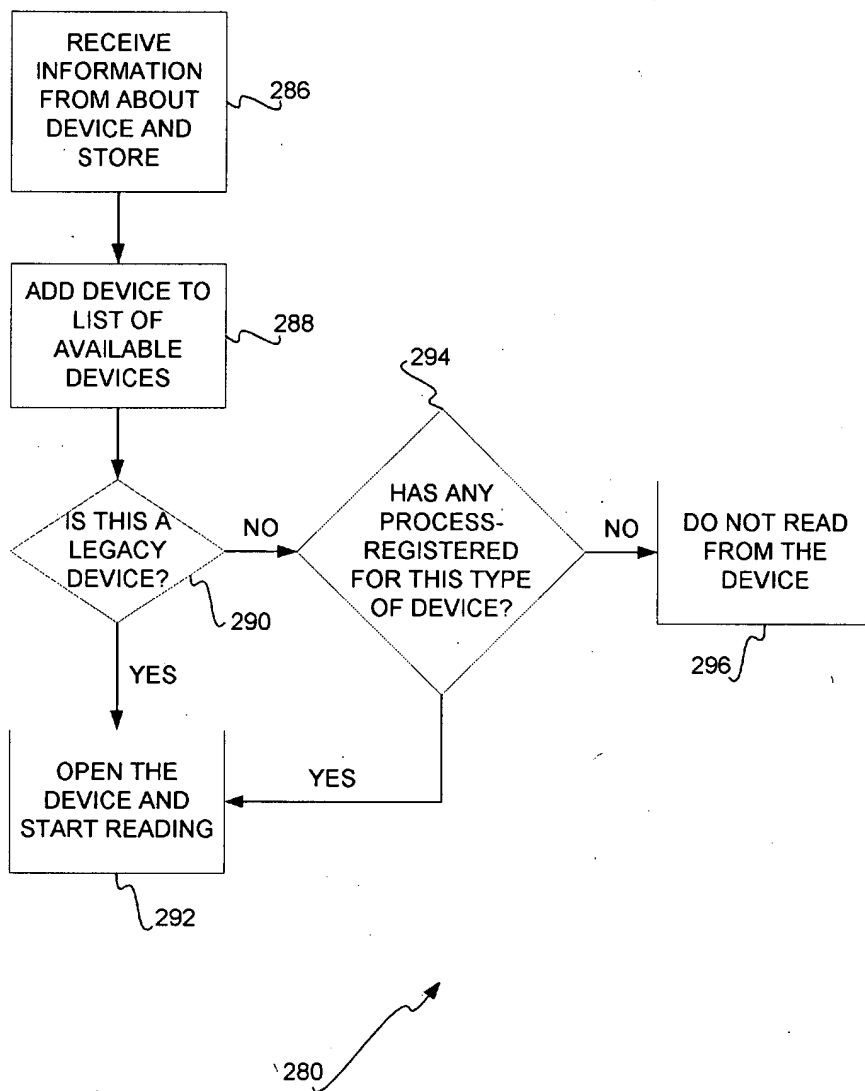


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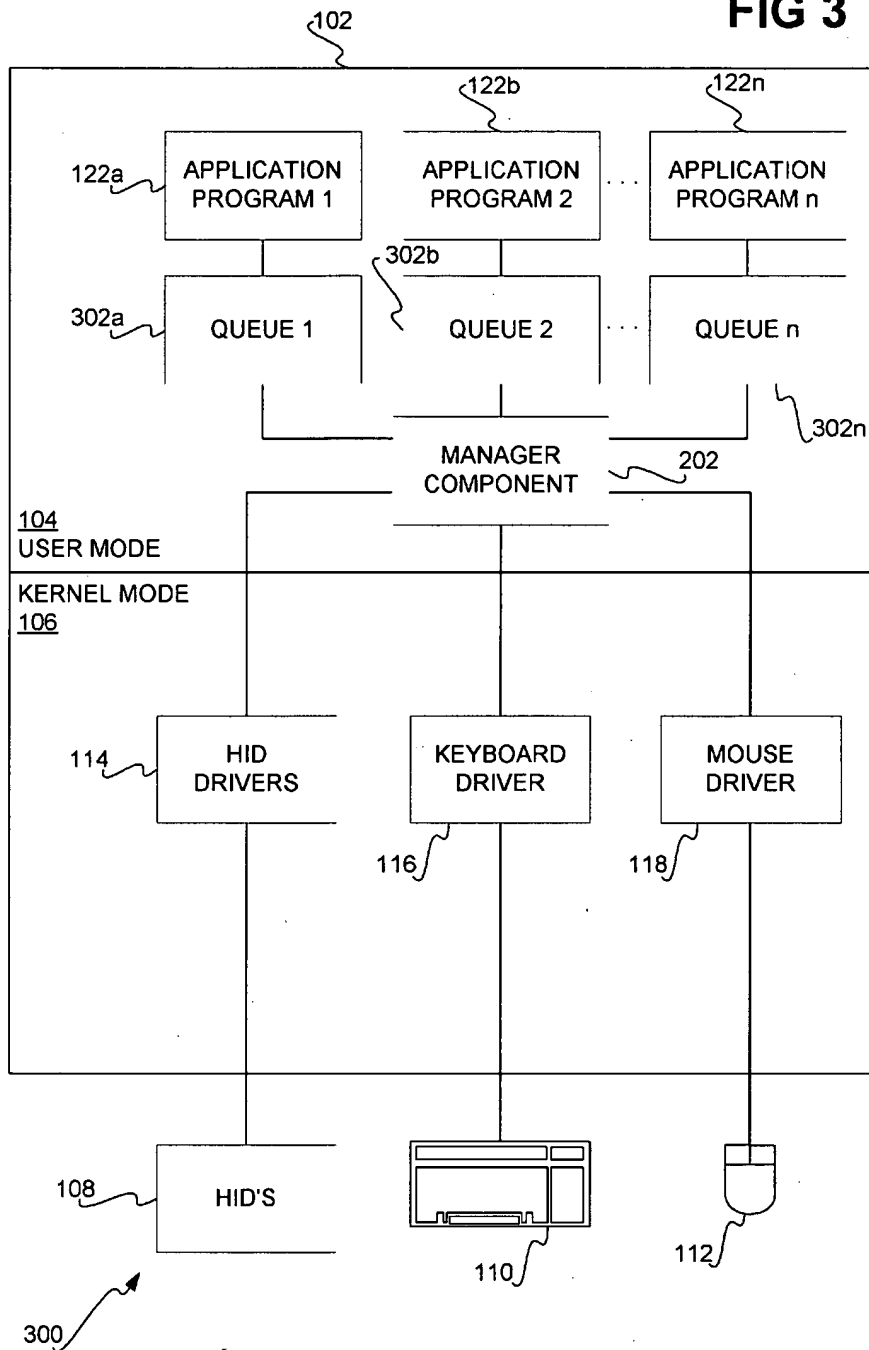
DEVICE ADDITION

FIG 2(e)



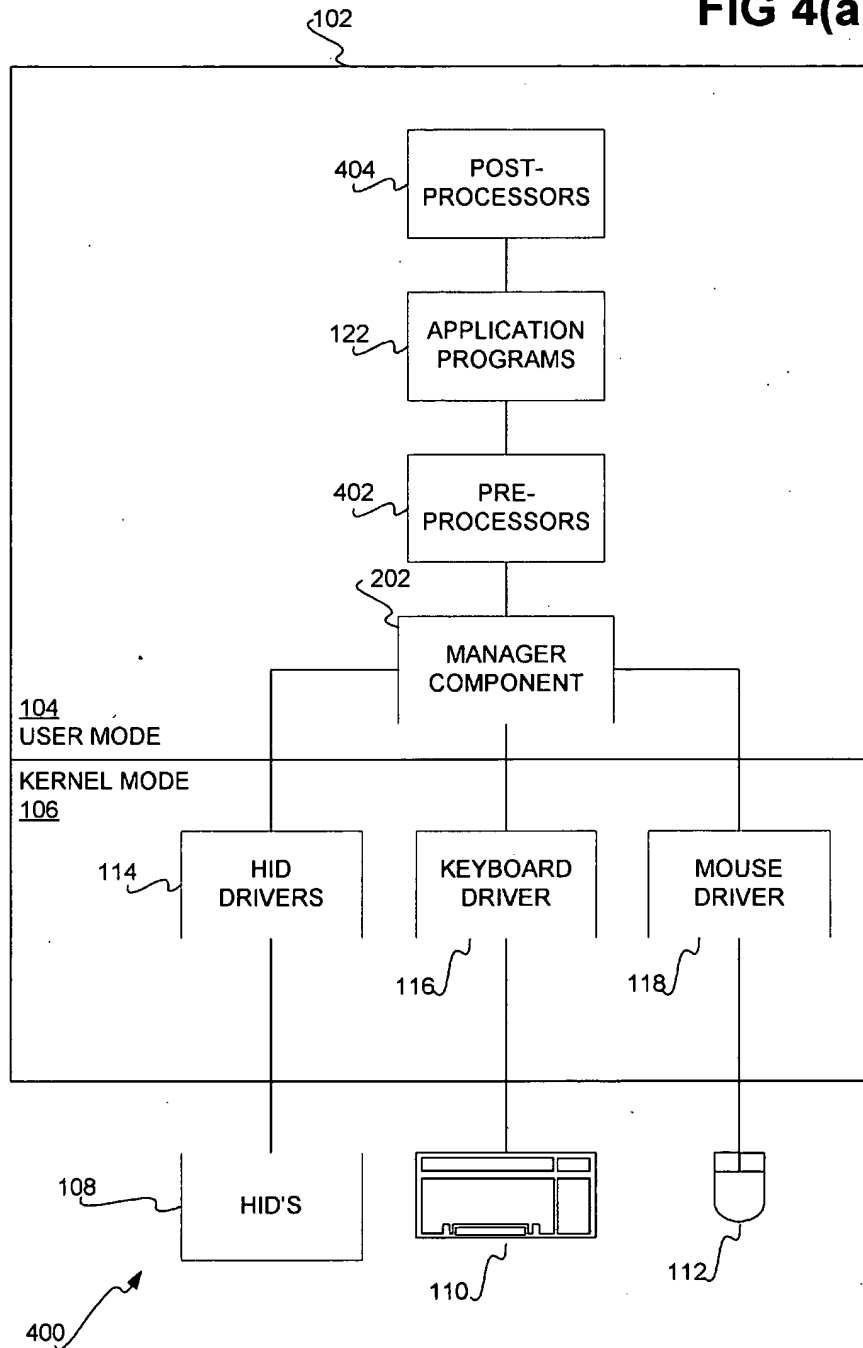
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FIG 3



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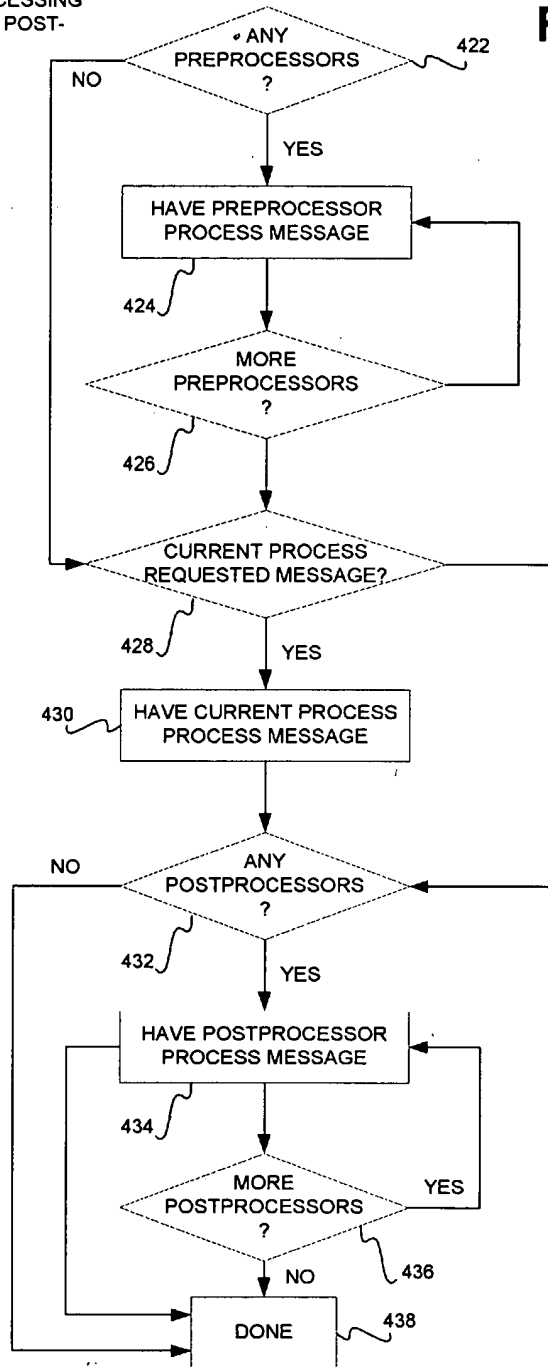
FIG 4(a)



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MESSAGE PROCESSING
WITH PRE- AND POST-
PROCESSORS

FIG 4(b)



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FIG 5

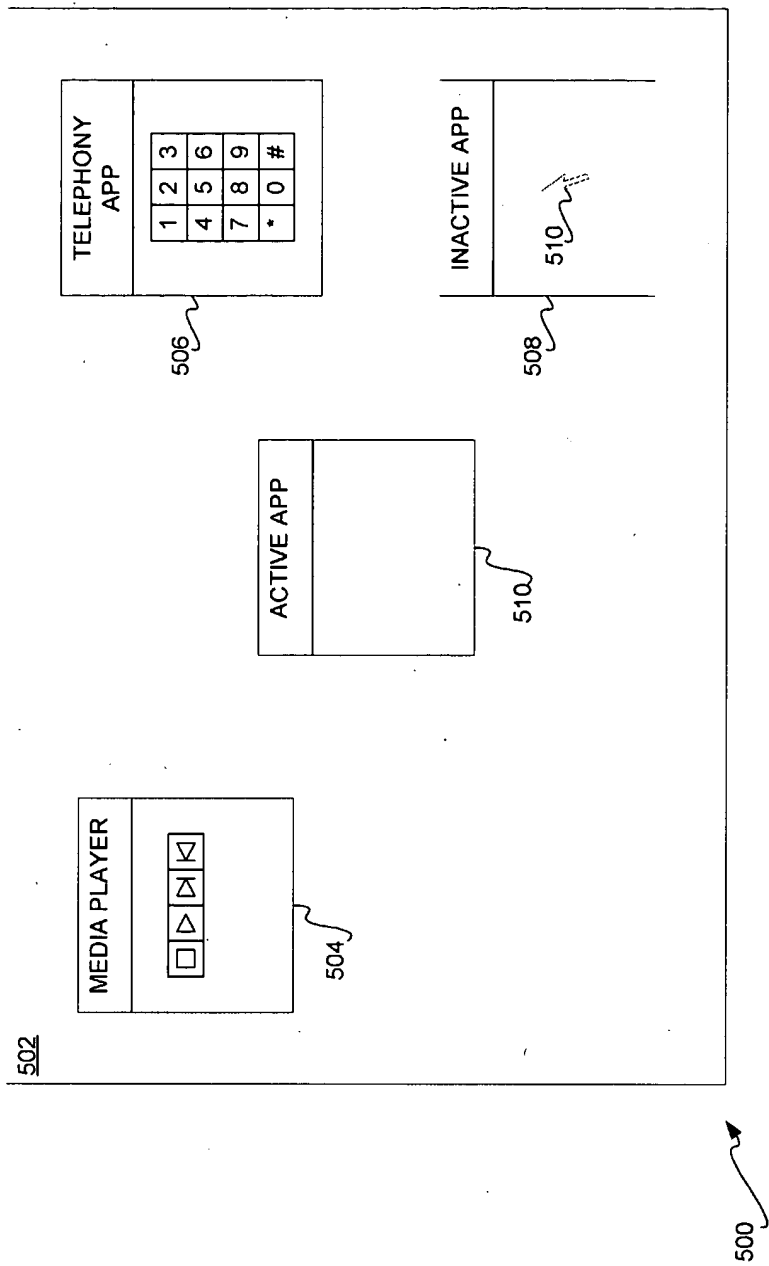


FIG 6

